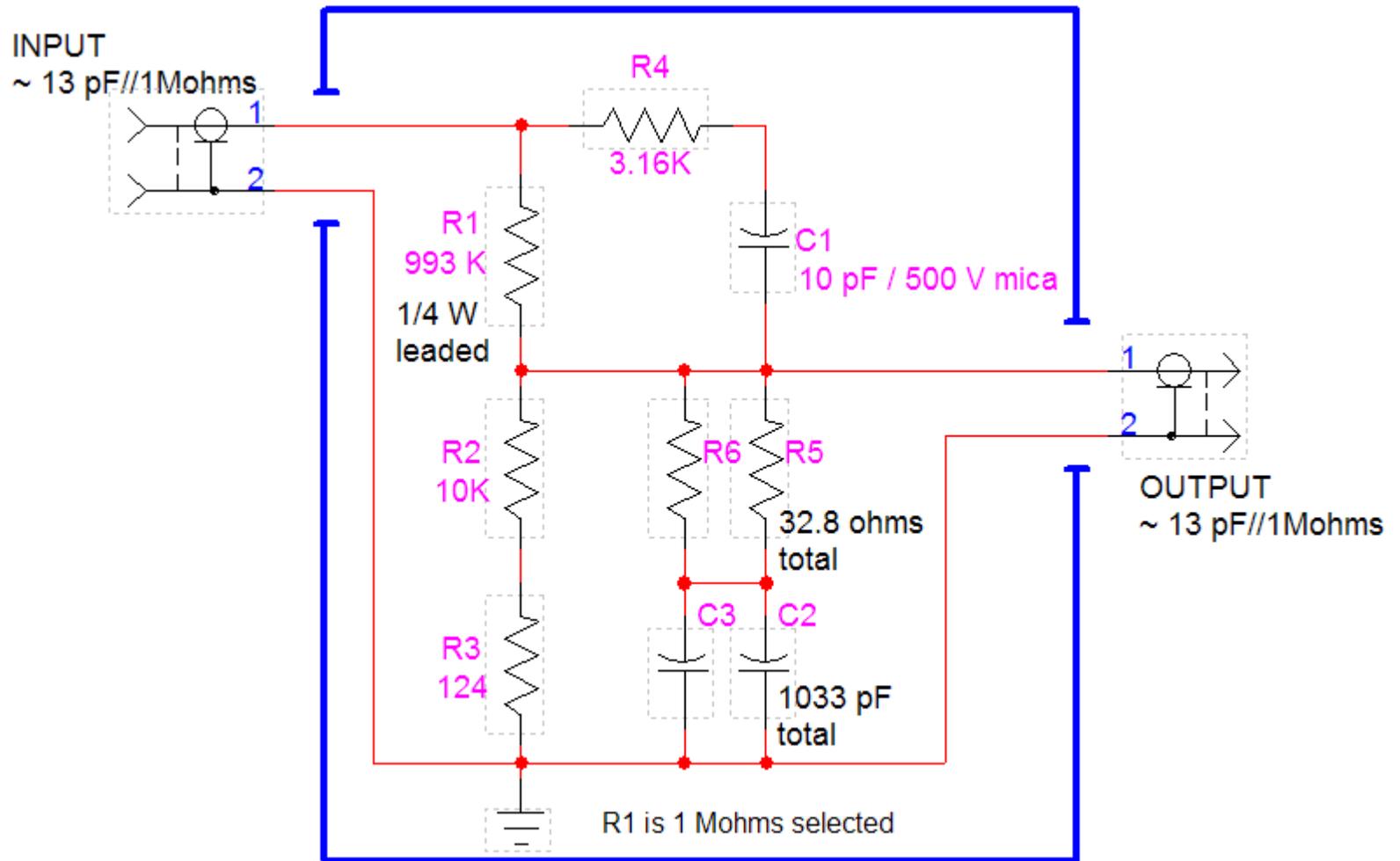
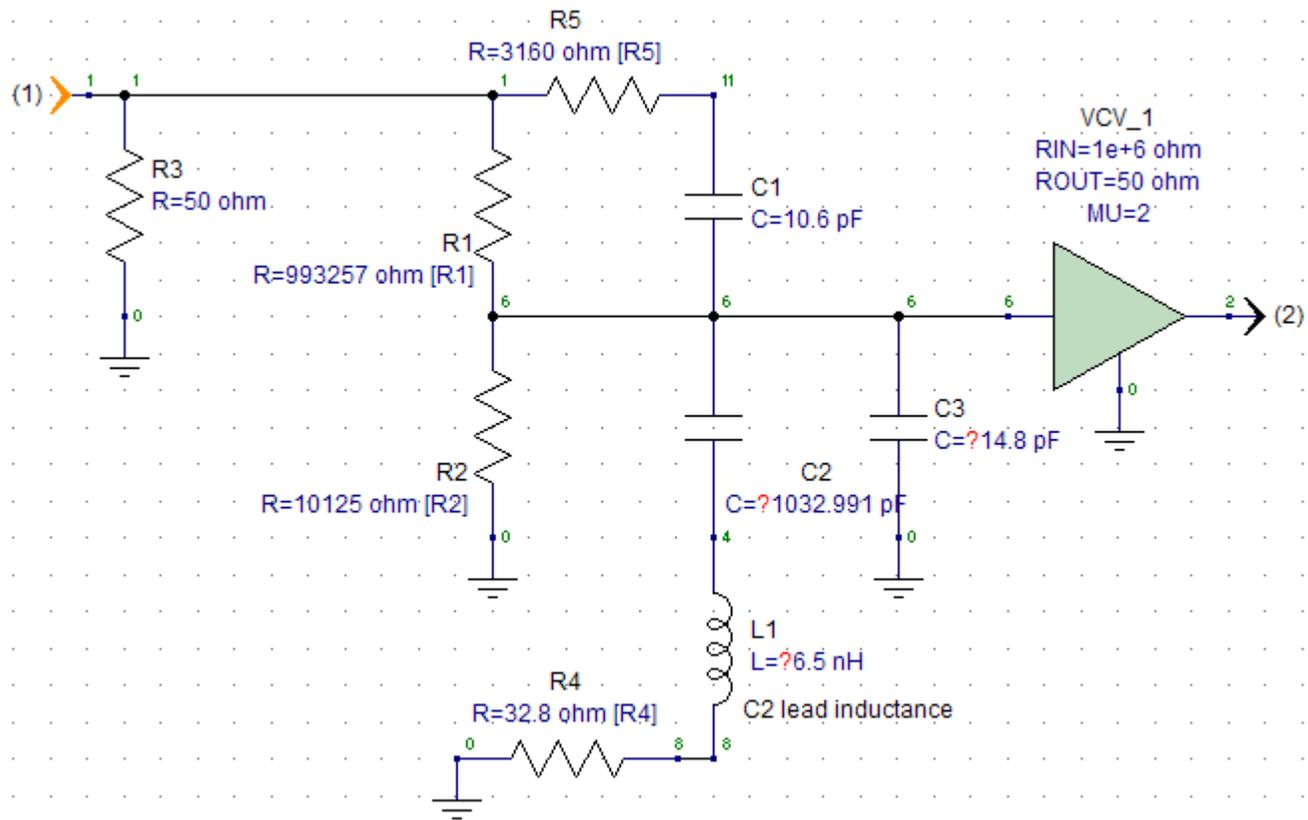


# 100 to 1 DIVIDER Circuit Schematic

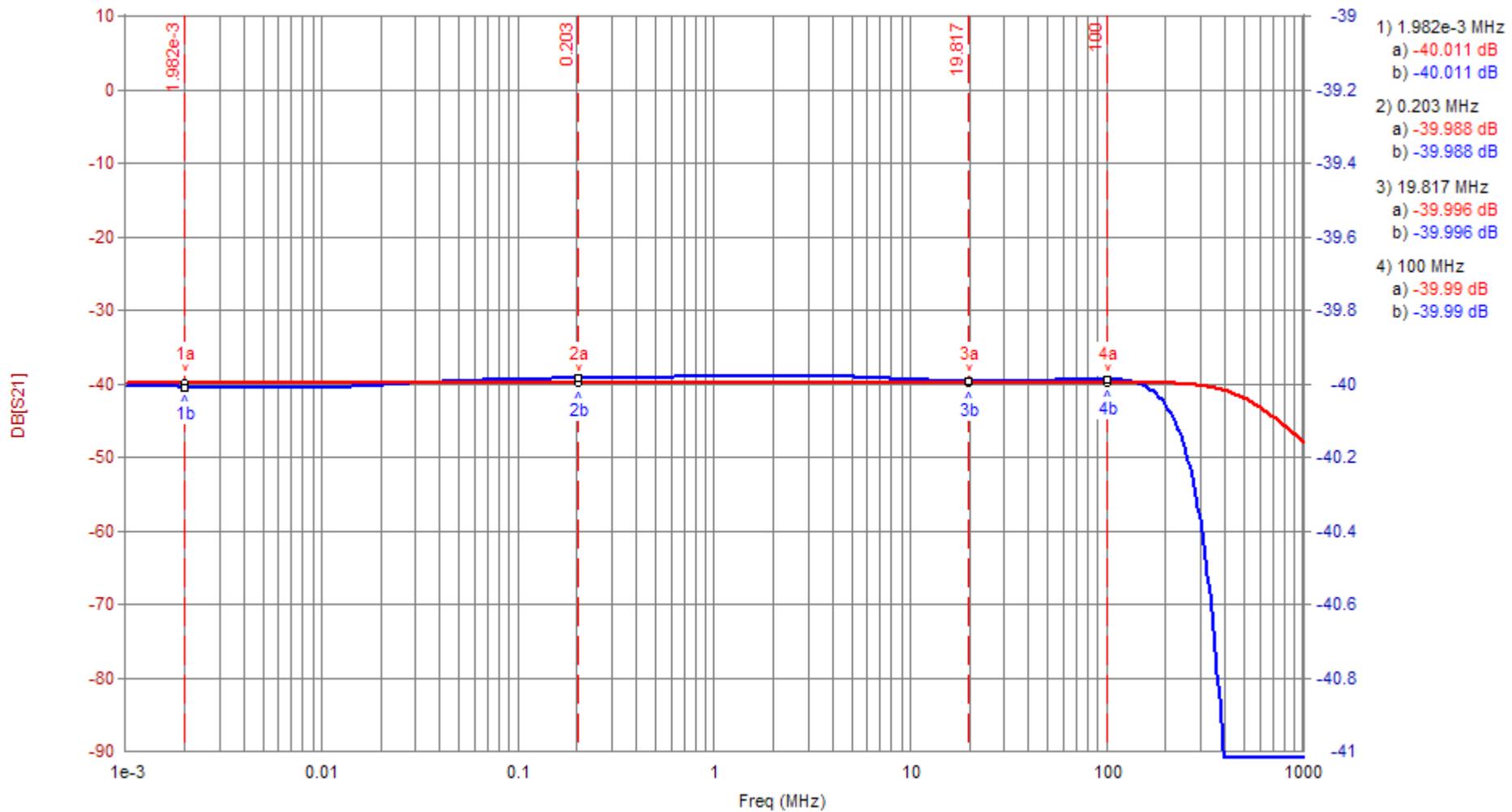
J. Audet VE2AZX ve2azx.net



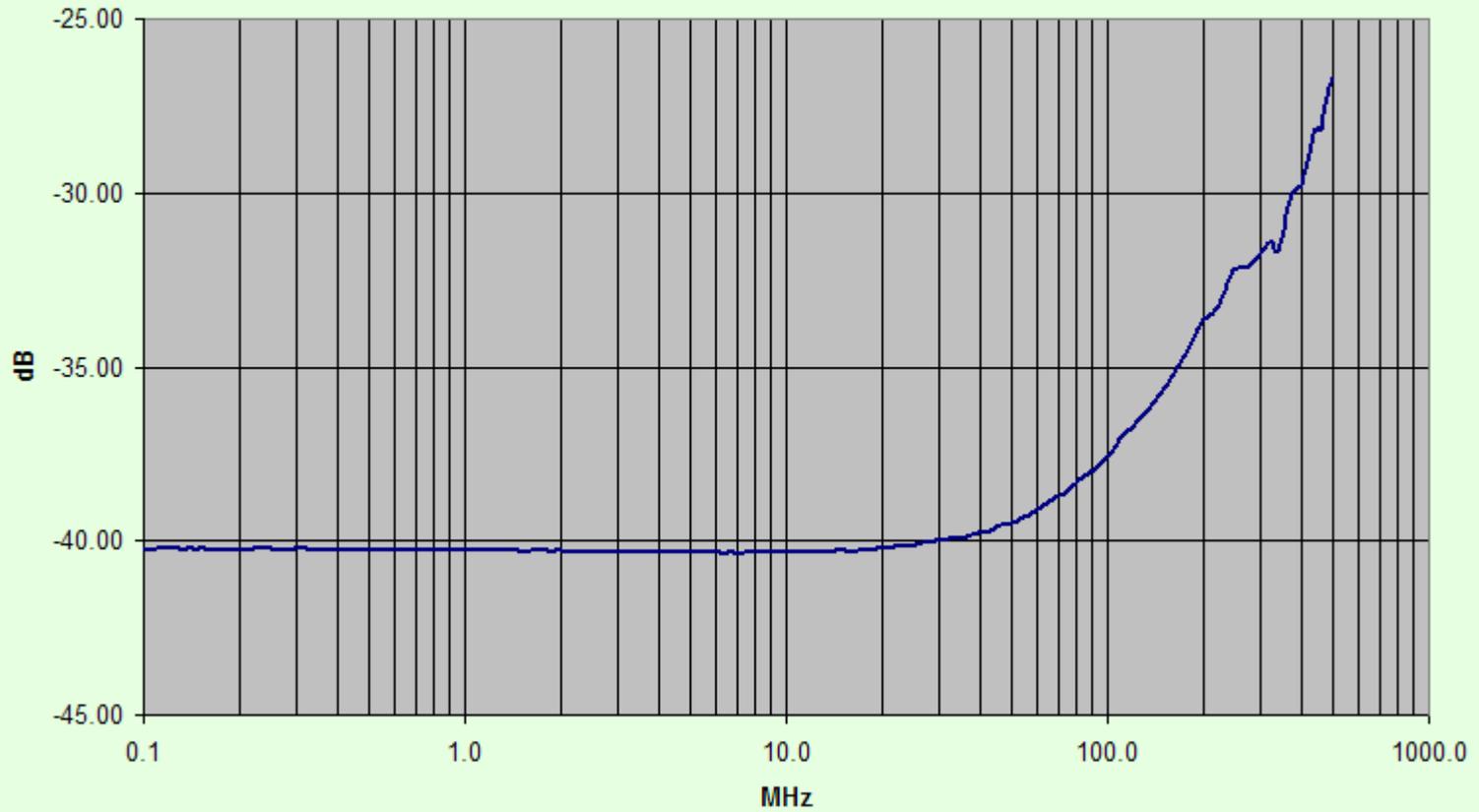
## Circuit for Simulation

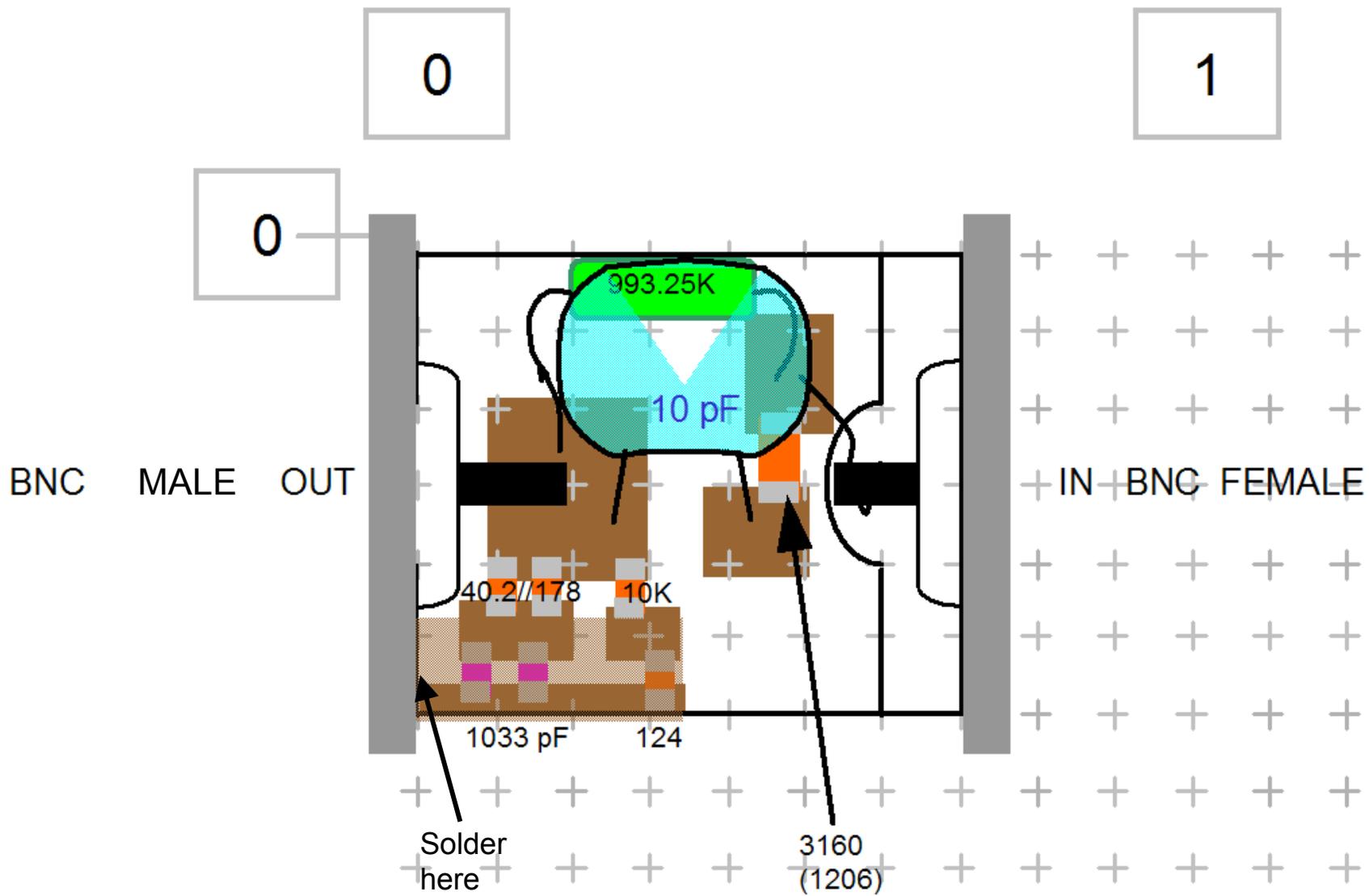


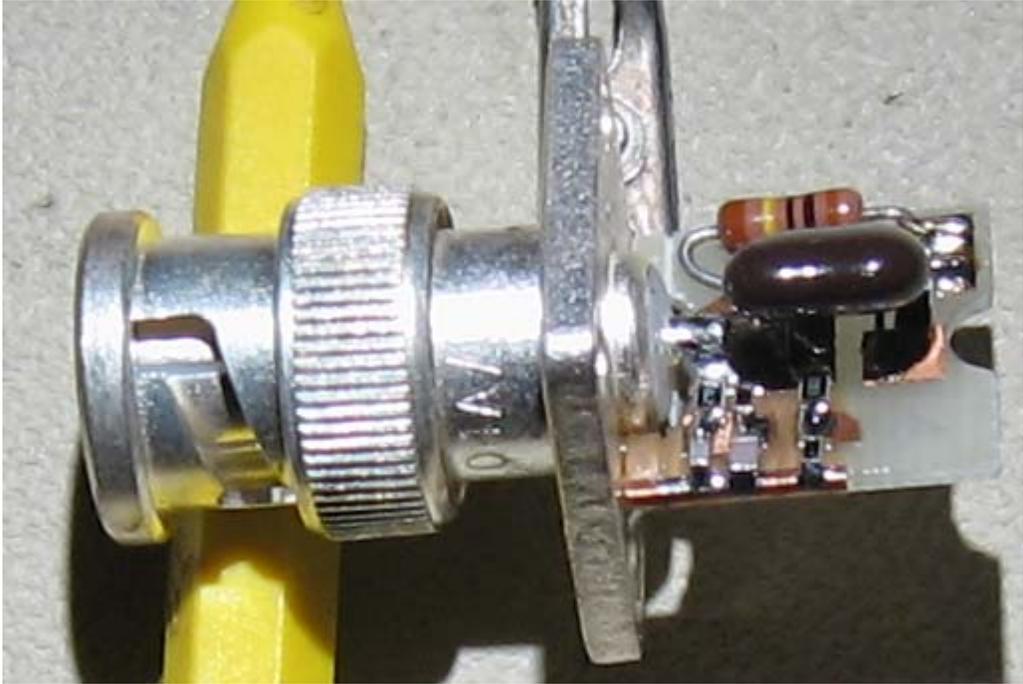
# Calculated Response



X100 Divider Measured Response











X100 DIVIDER

IN → OUT

1 MΩ / 13 pF